

MSPC 2012

Onur Mutlu
June 16, 2012
Beijing

Process

- 21 submissions
 - 16 full length
 - 5 position abstracts

- 12 accepted papers
 - 7 full length
 - 5 poster abstracts

- 5.95 reviews per paper

- PC meeting on April 21
 - 21/25 PC members present
 - Extensive online discussions; all papers discussed

Program Committee

Rajeev Balasubramonian, Utah
Emery Berger, UMass Amherst
John Carter, IBM Research
Jichuan Chang, HP Labs
Andrew Chien, Chicago
Trishul Chilimbi, Microsoft Research
Cliff Click, Azul Systems
Eiman Ebrahimi, UT-Austin
Mattan Erez, UT-Austin
Eugene Gorbatov, Intel
Nikos Hardavellas, Northwestern
Maurice Herlihy, Brown
Brian Hirano, Oracle

Hillery Hunter, IBM Research
Jim Larus, Microsoft Research
Alvy Lebeck, Duke
Thomas Moscibroda, Microsoft Research
Naveen Muralimanohar, HP Labs
Satish Narayanasamy, Michigan
Erez Petrank, Technion
Moinuddin Qureshi, Georgia Tech
Xipeng Shen, William and Mary
Osman Unsal, BSC
Chris Wilkerson, Intel
Ben Zorn, Microsoft Research

Programming languages, compilers, systems, distributed computing, and computer architecture

Thanks

- PC members
- Lixin Zhang
- Ben Zorn
- Kathryn McKinley
- Authors

MSPC 2012 Program

- 6 sessions
 - MSPC/ISMM Invited Talk (Erez Petrank)
 - Locality and Memory Models
 - Memory Scheduling
 - Memory Management
 - Poster Potpourri
 - Poster and Wild and Crazy Ideas
- All papers online
 - on MSPC website <http://safari.ece.cmu.edu/MSPC2012/>
 - on ACM Digital Library
- Enough time between sessions for discussion

MSPC/ISMM Invited Talk

- Erez Petrank, Technion
- Can Parallel Data Structures Rely on Automatic Memory Managers?
- Erez Petrank is a professor of computer science at the Technion. Erez's research interests are in the areas of memory management and parallel algorithms. He has been active in designing concurrent data structures and various memory management algorithms. He also participated in the construction of practical systems at IBM and at Microsoft.